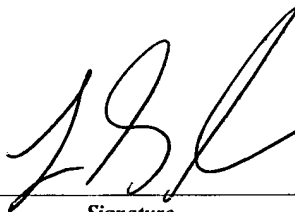
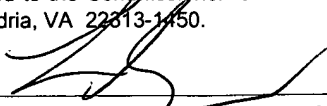


Image

Image/2811

TRANSMITTAL OF FORMAL DRAWINGS				Docket No. YOR920000588US2 (16966)	
In Re Application Of: Tak H. Ning					
Serial No. 09/757,965	Filing Date January 10, 2001	Confirmation No. 9386	Examiner Thien F. Tran	Art Unit 2811	
Invention: FULLY DEPLETED COLLECTOR SILICON ON INSULATOR (SOI) BIPOLAR TRANSISTOR USEFUL ALONE OR IN SOI BICMOS					
Address to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450					
Transmitted herewith are: 14 sheets of formal drawing(s) for this application. <input checked="" type="checkbox"/> Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).					
 Signature			Dated: March 24, 2004		
<div>Leslie S. Szivos, Ph.D. Registration No. 39,394 SCULLY, SCOTT, MURPHY & PRESSER 400 Garden City, New York 11530 Garden City, New York 11530 (516) 742-4343 LSS:HAH:kc</div>			<div>I certify that this document and attached formal drawings are being deposited on March 24, 2004 with the U.S. Postal Service as first class mail under 37 C.F.R. 1.8 and addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.  Signature of Person Mailing Correspondence Leslie S. Szivos, Ph.D. Typed or Printed Name of Person Mailing Correspondence</div>		